IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Robert H. Dennard, et al.

Examiner: Unes

Unassigned Das H. Nguyen

Serial No.:

10/659,950 To be assigned

Art Unit:

Unassigned

2818

Filed:

Herewith

Docket:YOR920020157US2 (15667A)

For: POLYSILICON BACK-GATED

Dated:

September 11, 2003

SOI MOSFET FOR DYNAMIC THRESHOLD

VOLTAGE CONTROL

Commissioner for Patents United States Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, applicants submit the following references which applicants believe may be material to the above-identified patent application. A copy of the references which applicants wish to make of record in this case is enclosed herein for the Examiner's convenience along with a listing on Form PTO-1449 attached.

1. U.S. Patent No. 6,074,920, issued June 2000 to Houston;

CERTIFICATE OF MAILING BY EXPRESS MAIL

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Date of Deposit: September 11, 2003

I hereby certify that this correspondence is being deposited with the United States Postal Service Express Mail Post Office to Addressee service under 37 C.F.R. §1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Alexandria, VA 22313-1450.

Dated: September 11, 2003

Leslie S. Szivos, Ph.D.

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80)PATENT AND TRADEMARK OFFICE LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)					Atty. Docket No. YOR920020157US2 (15667A)				Serial No. 10/659, 950 To be assigned			
					Applicant Robert H. Dennard, et al.							
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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)												
												
	 											
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